

Localization effects in the tunnel barriers of phosphorus-doped silicon quantum dots

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Abstract

We have observed a negative differential conductance with singular gate and source-drain bias dependences in a phosphorus-doped silicon quantum dot. Its origin is discussed within the framework of weak localization. By measuring the current-voltage characteristics at different temperatures as well as simulating the tunneling rates dependences on energy, we demonstrate that the presence of shallow energy defects together with an enhancement of localization satisfactory explain our observations. Effects observed in magnetic fields are also discussed.

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I. INTRODUCTION

The consequences of localization and disorder on the transport properties, in particular, on the mobility of devices have been investigated extensively in bulk and in two-dimensional semiconductors [1, 2]. One of the most noticeable and known effect is hopping conduction where electrons are allowed to hop between impurities sites, leading to a decrease in the material conductivity with temperature. This was first theoretically described by Abrahams [3] to explain the transport properties of random media. In this case, the boundary between ballistic and diffusive transport is well represented spatially by an ellipsoid which major and minor axis are given respectively by the hopping and localization lengths. Unless screened by a metal gate or short-range disorder, electron-electron interaction is often competing with localization, leading to a modification of the conductivity at low temperature [4] and eventually to a metal to insulator transition [5]. In nanostructures and, in particular in quantum dots, the localized region is defined by the device geometry, principally the number, the shape, the nature and the orientation of interfaces. Due to dielectric screening, the effective Bohr radius at trapping sites at the edges of the structure can significantly increase and electron localization becomes possible. Such phenomenon have been already observed at the periphery of dots fabricated within an AlGaAs-GaAs heterostructure [6] and Coulomb blockade has been found to be affected via a change in the dot capacitance.

In doped devices, like phosphorus-doped silicon single electron transistors, the localized region has a more profound effect owing to the presence of dopants at the edge of the structure and so, in close proximity to the Si-SiO₂ interface [7]. In presence of Coulomb interaction, charge rearrangement is possible and becomes an effective mean of reducing the charging energy. It may also be responsible for the observed reduction in the effective dot dimension due to electron trapping at the edge of the structure and the subsequent change in the electrostatic potential.

Despite this, the outer region of the dot is not the only place where localization has a non-negligible influence. Randomness in dopant distribution may lead to traps being present at or close to the tunnel barrier. As a result, the transmission probability, and so, the device conductivity, is expected to be significantly affected. The situation where a single dopant is unintentionally located at the barrier has already been investigated [8, 9] in silicon nanostructures. However, most studies report on resonant features following the alignment

of the quantum dot levels with the D_0 or D_- states of the impurity [10], a system that drives interest as it could be applied to spin filtering [11] in spin qubit architectures [12].

In this article, we have used a highly doped silicon single electron transistor (SET) and show that an additional localization process may lie at the tunnel barrier. Caused by natural defects or more generally, by an asymmetry in the barrier shape, the effect can enhance significantly the electron trapping mechanism. In the first section, we discuss the observation of a negative differential conductance as well as its dependence on gate voltage. We then describe the localization process through the temperature dependence of the conductivity and simulations. The analysis of experimental results allows distinguishing between static and dynamic effects in the tunnel barriers. In a second paragraph, we describe and interpret the magnetic field and temperature dependences in the low temperature regime before conclusions are drawn on the origin and potential implication of this effect.

II. DEVICE AND EXPERIMENTAL SETUP

Devices were fabricated from a silicon-on-insulator (SOI) substrate with a 40 nm-thick silicon layer that was subsequently implanted with phosphorus to obtain a dopant density of $3 \times 10^{19} \text{ cm}^{-3}$ after thermal anneal. At such a doping density, the largest areas of the contact leads remain metallic whereas high density defect surfaces partially deplete the nanostructures and locally increase the localization at its edges [7]. The quantum dot and its controlled in-plane gate V_g were defined by electron beam lithography during a single step process. The dot diameter was then reduced to about 120 nm after thermal oxidation, leading to the formation of a 20 nm oxide layer (Inset Fig. 1). All measurements were taken in an Oxford Instrument HelioxTM cryostat that was fitted with a 7 T magnet and whose base temperature was 300 mK. The current I_{SD} was measured using a Hewlett Packard 3458A multimeter with a 10^8 V/A amplifier on battery. The temperature of the device was controlled by an Oxford Instrument Intelligent Temperature Controller (ITC4) from 300 mK up to 150 K using RuO_2 and Cernox calibrated thermometers.

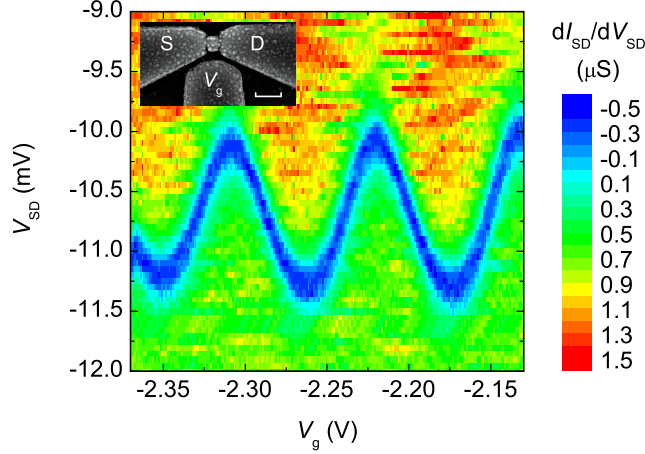


FIG. 1. Position of the NDC in V_{SD} and V_g at 300 mK. The inset shows a Scanning Electron Microscope (SEM) image of the device with source (S), drain (D) contacts as well as the SET gate V_g . The bar scale represents 200 nm.

III. EXPERIMENTAL RESULTS

A. Negative differential conductance

The device is characterized by regularly spaced Coulomb diamonds and so, it operates as a single quantum dot at low temperature. This will not be discussed here, but one may report to previous observations [15]. However, a negative differential conductance (NDC) can clearly be noticed in the region of negative source-drain bias V_{SD} (Fig. 1). Such feature could be found in single quantum dot where additional parallel channels via edge states are opened for tunneling or when trap-assisted tunneling is present [13] or in the case of spin dependent transport [14]. Here, the position of the NDC clearly follows the shape of the Coulomb diamonds, at a fixed distance from their edges. This rather suggests that the existence of the NDC is associated with tunneling events in the quantum dot. Previous studies on the same quantum dot have shown that such a NDC was offsetting the electron tunneling detection point of a nearby SET [15] and that an asymmetry in the tunneling rates could explain the observed phenomenon. Still, this observation alone does not enlighten on the fundamental electronic process responsible for such asymmetry in the tunneling rates and so, additional experiments are required.

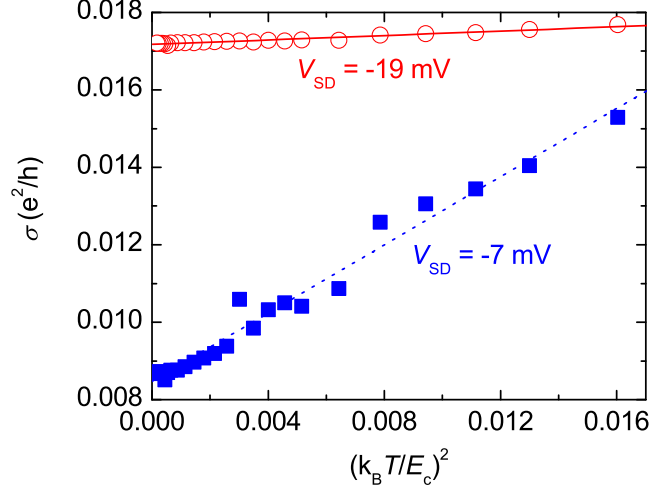


FIG. 2. Conductivity in the inelastic cotunneling regime at different values of V_{SD} .

B. Localization effects in the tunnel barrier

1. Barrier asymmetry

The asymmetry of the tunnel barrier rates can be assessed more directly by determining the dependence of the multi-electron tunneling rates for each barrier [16] $\Gamma_{s,D}$ on V_{SD} . Extracting separately the values for Γ_s and Γ_D from the total tunneling rate $I_{SD}/e = \Gamma_s \Gamma_D / (\Gamma_s + \Gamma_D)$ and the dependence of the current on temperature is generally challenging. This is due to the presence of both elastic and inelastic processes and their complex dependences on source-drain and gate biases.

However, it is possible to estimate the product of the tunneling rates by observing the quadratic dependence of the conductivity in temperature below 3 K, a behavior that is characteristic of inelastic cotunneling [17] (Fig. 2). Indeed, within this regime the conductivity is given by [18]:

$$\sigma_{SD} \approx \frac{2he^2}{3} \Gamma_s \Gamma_D \frac{1}{E_c^2 \Delta F^2} \left[(k_B T)^2 + \left(\frac{e V_{SD}}{2\pi} \right)^2 \right] \quad (1)$$

where E_c is the dot charging energy, $\Delta F = -e\alpha_D V_{SD} - e\alpha_g V_g$ the change in the free energy of the system on the tunneling of an electron in or out the quantum dot, α_D and α_g , the level arms of the drain and gate respectively.

On the other hand, it is possible to estimate the tunneling rates by taking into account the tunnel probabilities of an electron through a barrier at a specific energy level $E_N = NE_C + E_0$ in the quantum dot. The use of the parabolic barrier approximation [15] allows extending the range of validity of the tunnel rates to large V_{SD} . Following, a method similar to the one used for square barriers [19], we found :

$$\Gamma_D \propto \frac{4\Delta E}{h} \exp[\alpha(\Delta F + E_N) + \beta] f(e\alpha_D V_{SD}, T_e) \quad (2)$$

$f(E, T)$ is the Fermi distribution of the drain reservoir, T_e is the electron temperature, ΔE the level spacing of the dot. Coefficients α , β are given respectively by :

$$\alpha = -\frac{\pi}{2} W \left(\frac{2m^* V}{\hbar^2} \right)^{1/2} \quad (3)$$

$$\beta = \frac{\pi}{2} W \left(\frac{2m^*}{\hbar^2 V} \right)^{1/2} \quad (4)$$

where $m^* = 0.19m_e$ is the effective mass of silicon, V and W respectively the height and width of the tunnel barrier (defined at the Fermi energy of the reservoir).

The value of $\Gamma_S \Gamma_D$ is then calculated by summing over all possible tunneling events, including elastic and inelastic contributions for the different energy levels of the quantum dot (ground and first excited states). The behavior of $\Gamma_S \Gamma_D$ is well reproduced for both positive and negative V_{SD} (especially at small biases) (Fig. 3) if one takes into account the dependence of the electron temperature on source-drain bias due to acoustic phonons [20]:

$$T_e \approx T_0 \left[1 + \left(\frac{e\alpha_D}{3.53k_B T_0} \right)^{7/2} \frac{V_{SD}^4}{V_0^{1/2}} \right]^{2/7} \quad (5)$$

where T_0 is the phonon bath temperature and the normalization constant $V_0 = 1$ mV.

The drain and gate level arms can be extracted from Coulomb diamonds and we find $\alpha_g = 0.048$ and $\alpha_{SD} = 0.44$. From best fits, we obtain $W_S = 8.0$ nm and $W_D = 13.3$ nm for the source and drain barrier widths and, $V_S = 5.4$ meV and $V_D = 11.8$ meV respectively for the corresponding barrier heights. The values for the barrier widths are compatible with

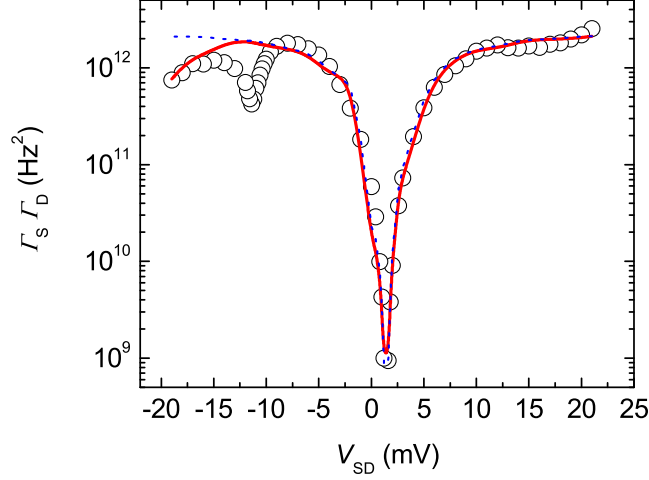


FIG. 3. Variation of the product of the tunneling rate of the source (Γ_S) and the drain (Γ_D) with source-drain bias and corresponding simulations for parabolic barriers (dotted lines) and in the case of energy dependent barrier width (solid line).

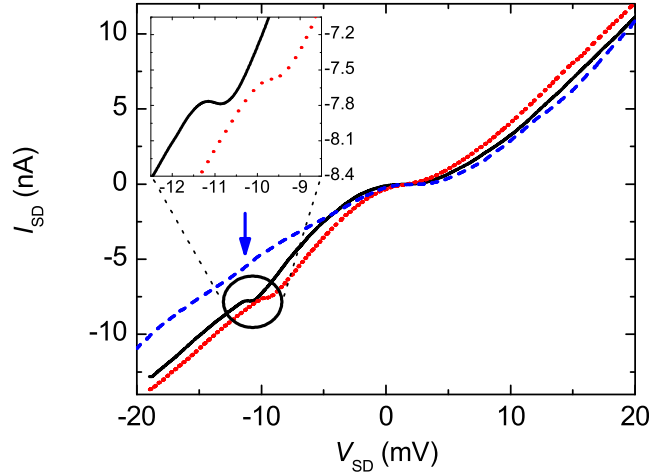


FIG. 4. Current-voltage characteristics before (solid line) and after thermal cycle to 150 K (dotted line) and to room temperature (dashed line).

the ones obtained from SEM observations and suggest the existence of an asymmetry in the device structure, and so, an asymmetry in the tunneling rates. It is interesting to notice that the potential barriers are small implying parallel conductive path at relatively small bias values, an effect explaining the additional conductivity background for $|V_{SD}| > 10\text{mV}$.

There is, however, a noticeable deviation for $V_{SD} < -10\text{ mV}$ between the simulated curve

using the parabolic approximation for the potential barrier and the experimental data (Fig. 3). This indicates that the tunnel rate is substantially decreased below this value. Because the tunneling time in a single square barrier is given by $\tau_s \sim \sinh(2\alpha W_s)$, where α is the attenuation constant, the potential barrier has to be substantially wider at energies lower than 5.3 meV (below the Fermi energy of the source lead when $V_{SD}=0$). This effect is well confirmed by simulations if electron tunneling is significantly decreased for energy levels lying below $-4E_C$. To investigate the origin of such barrier shape, and in particular, to distinguish between static (geometry) and dynamic (localization) effects, we proceeded with a series of thermal cycles (Fig. 4).

After raising the device temperature to 150 K and slowly cooling it back to 300 mK, no NDC is observed. Instead, a plateau is present in the current-voltage characteristics. Following a different cooldown from room temperature, no NDC nor plateau could be observed. These results point towards the presence of trapped electrons that have been thermally activated. Further thermal cycles up to room temperature did not change the shape of the current voltage characteristics and only an inflexion remains (arrow in Fig. 4). This indicates that both a geometrical asymmetry in the barrier and electron trapping during cooldown are likely to be responsible for the NDC.

2. *Enhancement of localization*

Additional information on the trapping mechanism is obtained by analyzing the temperature dependence of the conductivity in the high temperature regime. To this end, we proceeded to a thermal cycle between 300 mK and 150 K and measured the variation in the height of a Coulomb peak in temperature (Fig. 5). The behavior is significantly different depending on whether the temperature is increased (squares) or decreased (circles). In the first case, the conductivity is limited by cotunneling at low temperature, as shown before (Fig. 2), then follows an activation behavior between 5 and 15 K, with an associated energy $E_1 = 1.39$ meV. The most noticeable feature is the abrupt decrease in conductivity at $T_0 \sim 15$ K and the presence of a new activation process with an energy value $E_2 = 3.5$ meV above this temperature. In the second case, i. e. when the temperature is gradually decreased, the same activation process E_2 is present in the high temperature range but the process E_1 has disappeared. This suggests that E_2 is an intrinsic characteristic of the device, and

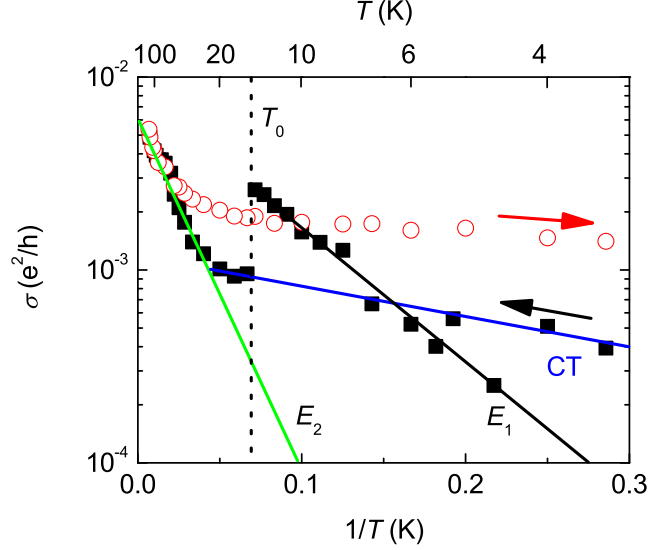


FIG. 5. $\sigma(T)$ with activated (solid lines) and cotunneling (CT) regimes when increasing and decreasing temperature from 3 to 150 K as shown by arrows. The dotted line shows the region of trap ionization associated with the electron delocalization temperature T_0 . $V_{SD} = 1.5$ mV and $V_g = -2.27$ V.

so, corresponds to an activation of donors in silicon, whereas E_1 is associated with the way electrons are localized to impurity states during the cooldown. Such an irreversible and abrupt delocalization of traps is supported by the fact that $k_B T_0 \sim E_1$. For single donors in silicon or at low concentration, the ionization energy is expected to be close to 45 meV. This value, however, decreases substantially with the donor separation, and, at a concentration of $\sim 10^{19} \text{ cm}^{-3}$ similar to the one used in these devices, it is presumably close to zero [21]. As a consequence, E_2 is likely to be associated with phosphorus donors at the Si/SiO₂ interface where localization effects may be more important. This increase of localization arises from the sensitivity of the ionization energy to the dielectric environment. Indeed, for degenerate silicon, we have [22]:

$$E_2 \sim \frac{1}{4\pi\epsilon_0} \frac{e^2}{2z} \frac{\epsilon_{\text{Si}} - \epsilon_{\text{SiO}_2}}{\epsilon_{\text{Si}} + \epsilon_{\text{SiO}_2}} \quad (6)$$

where z is the distance to the SiO₂-Si interface and ϵ_0 the dielectric constant in free space. Indeed for $z \sim 9$ nm, we obtain $E_2 \sim 3.5$ meV.

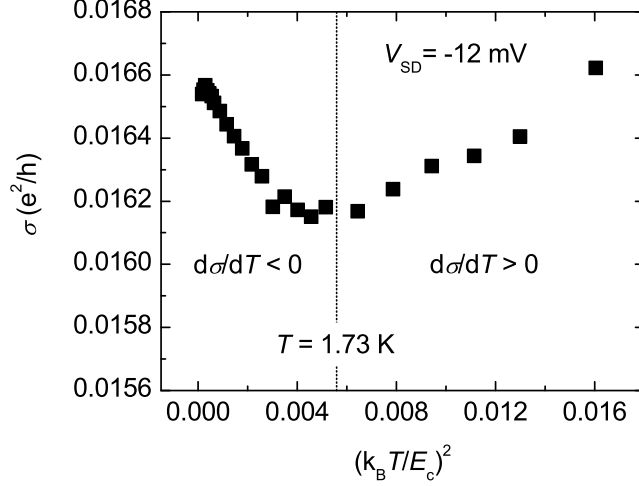


FIG. 6. Temperature dependence of the conductivity in the low temperature regime. A change in behavior is clearly noticeable at around 1.73 K. E_c is the charging energy of the quantum dot.

Finally, we notice that the NDC disappears irreversibly above 17 K, thus linking the electron localization in the barrier to the presence of an NDC. Still, this process alone cannot explain the observed phenomenon as a whole. Instead a combination of structural defects in the barrier and localization enhancement due to electron trapping may explain the behaviors observed in temperature and gate voltage dependences. Structural defects in the barrier may arise from dislocations or the presence of impurities, silicon dangling bond defects or the formation of a phosphorus clusters that localize electrons that may form either during the implantation or the annealing stage.

After thermal cycling, the low temperature conductivity is higher, implying that the strength of localization has been decreased successfully.

C. Low temperature and magnetic field dependences

1. Temperature effects

Despite the high doping concentration, the silicon quantum dot shows an insulating behavior down to 300 mK, for all gate voltages V_g i.e. the conductivity decreases with temperature (Figs. 2 and 5). However, for a small range of source-drain biases V_{SD} close in value to the the NDC position and in the absence of magnetic field, we observe an increase

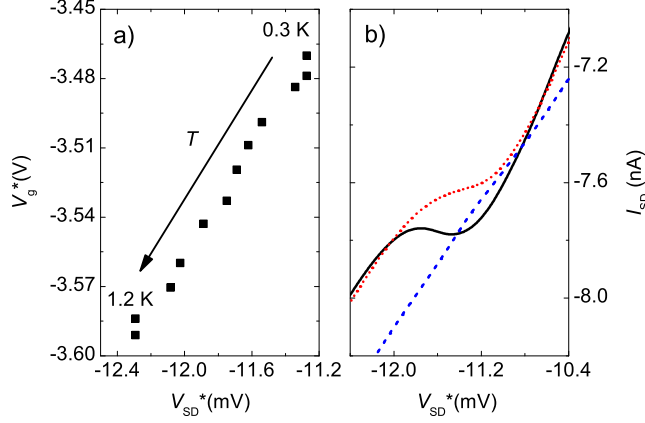


FIG. 7. (a) Variation of the position of the NDC in gate voltages with temperature. (b) Evolution of the shape and position of the NDC at 0.3 K (solid line), 1.5 K (dotted line) and 3 K (dashed line). The apparent shift towards positive V_{SD} is an effect of the displacement of the NDC in gate voltage, as described in the text.

in conductivity σ below $T_C = 1.73$ K ($\sim 149 \mu\text{eV}$) (Fig. 6). To understand the origin of the effect, we have measured the dependence of the conductivity on V_{SD} and V_g , similarly to Figure 1, but at different temperatures up to 3 K. We find that the NDC position (V_{SD}^* and V_g^*) is significantly affected by the temperature (Fig. 7a). Also, the NDC evolves into a plateau due to the presence of additional tunneling mechanisms above 1.5 K, a value close to T_C (Fig. 7b).

By raising the temperature of the device, the conductivity increases via cotunneling and the energy levels get broader, leading to a loss in the NDC visibility. The second effect of thermal broadening is a slight displacement of the NDC in source-drain bias of the order of $3.53 k_B T$ [23]. This corresponds to the thermal broadening of a Coulomb peak in the classical and high temperature regime, so the expected shift at 1.2 K is about $830 \mu\text{V}$, as measured (Fig. 7). The increase of temperature also modifies the tunneling rates via Eqs. 1 and 2, so that the fractional number of electrons in the quantum dot n becomes temperature dependent and

$$n = \frac{\Gamma_D}{\Gamma_D + \Gamma_S} \sim 1 - \gamma \exp(\delta E / k_B T) \quad (7)$$

where γ is a constant depending on the device characteristics and δE a function of V_{SD}

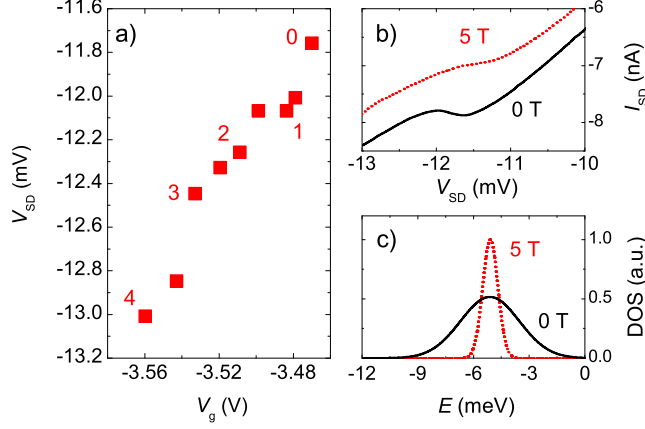


FIG. 8. a) NDC position at different magnetic fields (field number in red in inset) after thermal cycle. b) $I_{SD}(V_{SD})$ at 0 (solid line) and 5 T (dotted line) before thermal cycle and at a fixed V_g . The relative similar positions of the NDC is an effect of the dependence of the NDC position on B . c) Sketch of the effect of magnetic field on the density of states of the localized states located in the barrier.

and V_g .

Consequently, Coulomb diamonds shift along V_g with temperature, so is the position of the NDC, as observed experimentally. Since Figure 6 is obtained at fixed values of V_g and V_{SD} , the variation $d\sigma/dT$ reflects indeed the displacement of V_{SD}^* and V_g^* in the map of Figure 1, and consequently, the variation of the conductivity $\sigma(V_g, V_{SD})$ at low temperature.

2. Magnetic field effects

Similarly to temperature effects, the application of a magnetic field B perpendicular to the substrate modifies both the position of the NDC and its visibility (Fig. 8a). One may expect an increase in the localization strength at high B , and so, a more pronounced NDC. However, only an inflexion in the current voltage characteristic is visible (Fig. 8b). Unlike thermal cycles, this effect is reversible. The effect of the field does reduce the effective diameter of the quantum dot by localizing more strongly the electrons at the Si-SiO₂ interface. It also affects the capacitances values. However, this is the density of states (DOS) near the source barrier which is predominantly affected. The number of localized states in the barrier is conserved but the spread in energy level is reduced (band tails in the DOS disappearing)

(Fig. 8c), so the NDC is smoothed. Such a modification of the DOS with B also explains that the tunneling rates are locally increased when tunneling takes place. The effective electron number in the dot is then reduced and the NDC position in V_g is shifted accordingly. The combination between an increase in the charging energy and the disappearance of the DOS tails justifies the magnitude of the shift observed along V_{SD} .

IV. CONCLUSIONS

We have shown that the NDC that we observed could be explained by the presence of defects in one of the tunnel barrier and by an increase in the localization in that region due to electron trapping during the cooldown of the sample. Within this description, the change in the sign of $d\sigma/dT$ results from the combination of the presence of an NDC region at negative source-drain bias consecutive to localization effects and the displacement of its position in V_g and V_{SD} with temperature, an effect which is partly due to thermal broadening and a modification of the tunneling rates. The application of a perpendicular magnetic field mainly affects the DOS, leading to similar effects to the temperature, including a displacement of the NDC with respect to gate and source-drain biases and the disappearance of the NDC at high field. These effects may have implications on the electrostatic control over the tunnel barriers as well as the spin-dependent electron tunneling if such a method is used for reading out the spin states of an electron.

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